

1. An oscillator having a fundamental frequency and having low phase noise comprising:

a frequency dependent amplifier;

a frequency dependent feedback device in communication with an output of said frequency dependent amplifier and an input of said frequency dependent amplifier; and

an attenuating device in communication with said frequency dependent amplifier to attenuate noise signals having a frequency much less than the fundamental frequency.

2. The oscillator of claim 1 wherein said attenuating device has a characteristic such that the fundamental frequency is from approximately ten times to twenty times a high pass bandwidth of a combination of the frequency dependent amplifier and the attenuating device.

3. The oscillator of claim 1 wherein said amplifier amplifies an input by a predetermined gain factor, and

wherein said frequency dependent feedback device comprises:

- a frequency dependent gain determining ^{impedance} in communication with said amplifier, wherein a maximum gain of said frequency dependent amplifier occurs at the fundamental frequency.

4. The oscillator of claim 3 wherein said amplifier comprises:

a pair of cross-coupled MOS transistors having a drain of each of said pair of cross-coupled MOS transistors being in communication with a gate of the other of said pair of cross-coupled MOS transistors and to a corresponding terminal of said frequency dependent gain determining impedance;

a first current source having a first terminal in communication with a source of a first one of said pair of cross-coupled MOS transistors and to a first terminal of said attenuating device; and

a second current source having a first terminal in communication with a source of a second one of said pair of cross-coupled MOS transistors and to a second terminal of said attenuating device.

5. The oscillator of claim 3 wherein said frequency dependent gain determining impedance comprises:

at least one inductor in communication with said amplifier and a first terminal of a voltage source; and

at least one capacitor in communication with said amplifier and a second terminal of the voltage source.

6. The oscillator of claim 4 wherein said attenuating device comprises a capacitor.

wherein said amplifier comprises:

having a drain of each of said first pair of cross-coupled MOS

first pair of cross-coupled MOS transistors and to a corresponding

a first current source in communication with a source of one of said

type and with a first terminal of said attenuating device;

one of said first pair of cross-coupled MOS transistors of the first

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device;
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conductivity type whereby a drain of each of said second pair of

of said second pair of cross-coupled MOS transistors and to one

a third current source in communication with a source of one of said

terminal of said second attenuating device; and

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a fourth current source in communication with a source of the other of said second pair of cross-coupled MOS transistors of the second conductivity type and with a fourth second terminal of said second attenuating device.

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8. The oscillator of claim 7 wherein said attenuating device comprises a first capacitor.

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9. The oscillator of claim 7 wherein said second attenuating device comprises a second capacitor.

10. An LC oscillator having a fundamental frequency and having low phase noise comprising:

a frequency dependent amplifier comprising:

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a pair of cross-coupled MOS transistors of a first conductivity type, a drain of each of said pair of cross-coupled MOS being in communication with a gate of the other one of said pair of cross-coupled MOS transistors,

a first current source in communication with a source of one of

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said pair of cross-coupled MOS transistors, and

a second current source in communication with a source of

another of said pair of cross-coupled MOS transistors;

a frequency dependent gain determining circuit comprising

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a first inductor in communication with the drain of said one of
said pair of cross-coupled MOS transistors and a first
terminal of a voltage source,
5 a second inductor in communication with the drain of the other
of said pair of cross-coupled MOS transistors and the first
terminal of the voltage source,
a first capacitor in communication with the drain of said one of
said pair of cross-coupled MOS transistors and a
second terminal of the voltage source, and
10 a second capacitor in communication with the drain of the other
of said pair of cross-coupled MOS transistors and the
second terminal of the voltage source; and
an attenuating circuit in communication with said frequency
dependent amplifier to reduce the gain of signals having
15 frequencies less than the fundamental frequency to decrease
the phase noise, wherein said attenuating circuit comprises a
third capacitor in communication with said first and second
current sources.

- 20 11. The LC oscillator of the claim 10 wherein said attenuating device has a
characteristic such that the fundamental frequency is from approximately
ten times to twenty times a high pass bandwidth of a combination of said
frequency dependent amplifier and said attenuating device.

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12. The LC oscillator of the claim 10 wherein said amplifier further comprises:
- a second pair of cross-coupled MOS transistors of a second conductivity type each having a drain connected to a gate of the other of second pair of cross-coupled MOS,
 - a third current source in communication with a source of one of said second pair of cross-coupled MOS transistors, and
 - a fourth current source in communication with a source of the other of said second pair of cross-coupled MOS.

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13. The LC oscillator of claim 12 wherein said attenuating circuit further comprises a fourth capacitor in communication with said third and fourth current sources.

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14. An RF communication device comprising:
- an oscillator having a fundamental frequency and having low phase noise comprising:
 - a frequency dependent amplifier;
 - a frequency dependent feedback device in communication with an output of said frequency dependent amplifier and an input of said frequency dependent amplifier; and
 - an attenuating device in communication with said frequency dependent amplifier to attenuate noise signals having a frequency
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19. The RF communication device of claim 18 wherein said amplifier comprises:

5 a pair of cross-coupled MOS transistors having a drain of each of said pair of cross-coupled MOS transistors being in communication with a gate of the other of said pair of cross-coupled MOS transistors and to a corresponding terminal of said frequency dependent gain determining impedance;

10 a first current source having a first terminal in communication with a source of a first one of said pair of cross-coupled MOS transistors and with a first terminal of said attenuating device; and

15 a second current source having a first terminal in communication with a source of a second one of said pair of cross-coupled MOS transistors and with a second terminal of said attenuating device.

20. The RF communication device of claim 18 wherein said frequency dependent gain determining impedance comprises:

at least one inductor in communication with said amplifier and a first terminal of a voltage source; and

20 at least one capacitor in communication with said amplifier and a second terminal of the voltage source.

21. The RF communication device of claim 19 wherein said attenuating device

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22. The RF communication device of claim 19 further comprising a second attenuating device, and

wherein said amplifier comprises:

a first pair of cross-coupled MOS transistors of a first conductivity type having a drain of each of said first pair of cross-coupled MOS transistors being in communication with a gate of the other of said first pair of cross-coupled MOS transistors and to a corresponding terminal of said frequency dependent gain determining impedance;

a first current source in communication with a source of one of
said first pair of cross-coupled MOS transistors of the first
conductivity type and with a first terminal of said attenuating
device;

a second current source in communication with a source of a second one of said first pair of cross-coupled MOS transistors of the first conductivity type and with a second terminal of said attenuating device;

a second pair of cross-coupled MOS transistors of a second conductivity type wherein a drain of each of said second pair of cross-coupled MOS transistors is connected to a gate of the other of said second pair of cross-coupled MOS

transistors and to one terminal of said frequency dependent gain determining impedance;

a third current source in communication with a source of one of said second pair of cross-coupled MOS transistors and with a first terminal of said second attenuating device; and a fourth current source in communication with a source of the other of said second pair of cross-coupled MOS transistors of the second conductivity type and to a fourth second terminal of said second attenuating device.

23. The RF communication device of claim 22 wherein said attenuating device comprises a first capacitor.

24. The RF communication device of claim 22 wherein said second attenuating device comprises a second capacitor.

25. A frequency transforming apparatus having low phase noise comprising:
a first oscillator having a first fundamental frequency comprising:

a first frequency dependent amplifier;

a first frequency dependent feedback device in communication

with an output of said first frequency dependent amplifier

and an input of said first frequency dependent amplifier; and

a first attenuating device in communication with said first

a second oscillator having a second fundamental frequency comprising:

a second frequency dependent feedback device in

a second attenuating device in communication with said second frequency dependent amplifier to attenuate noise signals having a frequency much less than the second fundamental frequency; and

a third frequency amplifier, and

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26. The frequency transforming apparatus of claim 25 wherein said first and second attenuating devices each have a respective characteristic such that the first and second fundamental frequencies are from approximately ten times to twenty times a high pass bandwidth of a respective combination of said first and second frequency dependent amplifiers, said first and second attenuating devices and said third frequency dependent amplifier and said third attenuating device.

27. The frequency transforming apparatus of claim 25 wherein said first, second and second frequency dependent amplifiers each amplifies an input by a respective predetermined gain factor,
wherein said first frequency dependent feedback device comprises:
a first frequency dependent gain determining impedance in communication with said first amplifier, wherein a first maximum gain of said first frequency dependent amplifier occurs at the first fundamental frequency, and
wherein said second frequency dependent feedback device comprises:

a second frequency dependent gain determining impedance in communication with said second amplifier, wherein a second maximum gain of said second frequency dependent amplifier occurs at the second fundamental frequency.

28. The frequency transforming apparatus of claim 27 wherein said first amplifier comprises:

5 a first pair of cross-coupled MOS transistors having a drain of each of said first pair of cross-coupled MOS transistors being in communication with a gate of the other of said first pair of cross-coupled MOS transistors and to a corresponding terminal of said first frequency dependent gain determining impedance;

10 a first current source having a first terminal in communication with a source of a first one of said first pair of cross-coupled MOS transistors and with a first terminal of said first attenuating device; and

15 a second current source having a first terminal in communication with a source of a second one of said first pair of cross-coupled MOS transistors and with a second terminal of said first attenuating device, and

wherein said second amplifier comprises:

20 a second pair of cross-coupled MOS transistors having a drain of each of said second pair of cross-coupled MOS transistors being in communication with a gate of the other of said second pair of cross-coupled MOS transistors and to a corresponding terminal of said second frequency

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dependent gain determining impedance;
a third current source having a first terminal in communication
with a source of a first one of said second pair of cross-
coupled MOS transistors and with a first terminal of said
second attenuating device; and
a fourth current source having a first terminal in communication
with a source of a second one of said second pair of cross-
coupled MOS transistors and with a second terminal of said
second attenuating device.

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29. The frequency transforming apparatus of claim 27 wherein each of said
first and second frequency dependent gain determining impedance
comprises:

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at least one inductor in communication with a respective one of said
first and second amplifiers and a first terminal of a voltage source;
and
at least one capacitor in communication with said a respective one of
said first and second amplifiers and a second terminal of the
voltage source.

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30. The frequency transforming apparatus of claim 29 wherein the first,
second and third attenuating devices each comprises a capacitor.

wherein said first and second amplifiers each comprises:

a first current source in communication with a source of one of said first pair of cross-coupled MOS transistors of the first conductivity type and with a first terminal of a respective one of said attenuating devices;

a second current source in communication with a source of a second one of said first pair of cross-coupled MOS transistors of the first conductivity type and with a second terminal of a respective one of said attenuating devices;

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gain determining impedances;

a third current source in communication with a source of one of
said second pair of cross-coupled MOS transistors and with a
first terminal of a respective one of said other attenuating
devices; and

a fourth current source in communication with a source of the other
of said second pair of cross-coupled MOS transistors of the
second conductivity type and to a fourth second terminal of a
respective one of said other attenuating device.

32. The frequency transforming apparatus of claim 31 wherein said
attenuating device and said other attenuating device each comprises a
first capacitor.

33. The frequency transforming apparatus of claim 32 wherein said
attenuating device and said other attenuating device each comprises a
second capacitor.

34. The frequency transforming apparatus of claim 25 wherein said frequency
dependent coupling circuit is selected from the group of coupling circuits
consisting of phase shifters, frequency mixers, frequency shifters,
modulators, and demodulators.

an input of the second oscillator, said frequency dependent

coupling circuit comprising:

a third frequency amplifier, and

a second attenuating device in communication with said third frequency dependent amplifier to attenuate noise signals having frequencies much less than the third fundamental frequency.

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36. The multiple frequency oscillation circuit of claim 35 said first and second attenuating devices each has a respective characteristic such that the first and second fundamental frequencies are from approximately ten times to twenty times a high pass bandwidth of a respective combination of said first and second frequency dependent amplifiers, said first and second attenuating devices and said third frequency dependent amplifier and said third attenuating device.

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37. The multiple frequency oscillation circuit of claim 35 wherein said first, second and second frequency dependent amplifiers each amplifies an input by a respective predetermined gain factor, wherein said first frequency dependent feedback device comprises:

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a first frequency dependent gain determining impedance in

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communication with said first amplifier, wherein a first maximum gain of said first frequency dependent amplifier occurs at the first fundamental frequency, and wherein said second frequency dependent feedback device

comprises:

a second frequency dependent gain determining impedance in
communication with said second amplifier, wherein a second
maximum gain of said second frequency dependent amplifier
occurs at the second fundamental frequency

38. The multiple frequency oscillation circuit of claim 37 wherein said first
amplifier comprises:

a first pair of cross-coupled MOS transistors having a drain of each of
said first pair of cross-coupled MOS transistors being in
communication with a gate of the other of said first pair of cross-
coupled MOS transistors and to a corresponding terminal of said
first frequency dependent gain determining impedance;

a first current source having a first terminal in communication with a
source of a first one of said first pair of cross-coupled MOS
transistors and with a first terminal of said first attenuating device;
and

a second current source having a first terminal in communication with
a source of a second one of said first pair of cross-coupled MOS
transistors and with a second terminal of said first attenuating
device, and

wherein said second amplifier comprises:

a second pair of cross-coupled MOS transistors having a drain

of each of said second pair of cross-coupled MOS transistors being in communication with a gate of the other of said second pair of cross-coupled MOS transistors and to a corresponding terminal of said second frequency dependent gain determining impedance;

a third current source having a first terminal in communication with a source of a first one of said second pair of cross-coupled MOS transistors and with a first terminal of said second attenuating device; and

a fourth current source having a first terminal in communication with a source of a second one of said second pair of cross-coupled MOS transistors and with a second terminal of said second attenuating device.

39. The multiple frequency oscillation circuit of claim 37 wherein each of said first and second frequency dependent gain determining impedances comprises:

at least one inductor in communication with a respective one of said first and second amplifiers and a first terminal of a voltage source;

and

at least one capacitor in communication with said a respective one of said first and second amplifiers and a second terminal of the voltage source.

40. The multiple frequency oscillation circuit of claim 39 wherein said first, second and third attenuating devices each comprises a capacitor.

5 41. The multiple frequency oscillation circuit of claim 37 wherein said first and second oscillators each comprises another attenuating device, wherein said first and second amplifiers each comprises:

10 a first pair of cross-coupled MOS transistors of a first conductivity type having a drain of each of said first pair of cross-coupled MOS transistors being in communication with a gate of the other of said first pair of cross-coupled MOS transistors and to a corresponding terminal of a respective one of said frequency dependent gain determining impedances;

15 a first current source in communication with a source of one of said first pair of cross-coupled MOS transistors of the first conductivity type and with a first terminal of a respective one of said attenuating devices;

20 a second current source in communication with a source of a second one of said first pair of cross-coupled MOS transistors of the first conductivity type and with a second terminal of a respective one of said attenuating devices;

a second pair of cross-coupled MOS transistors of a second

conductivity type whereby a drain of each of said second pair of cross-coupled MOS transistors is connected to a gate of the other of said second pair of cross-coupled MOS transistors and to one terminal of a respective one of said frequency dependent gain determining impedances;

a third current source in communication with a source of one of said second pair of cross-coupled MOS transistors and with a first terminal of a respective one of said other attenuating devices; and

a fourth current source in communication with a source of the other of said second pair of cross-coupled MOS transistors of the second conductivity type and to a fourth second terminal of a respective one of said other attenuating device.

42. The multiple frequency oscillation circuit of claim 41 said attenuating device and said other attenuating device each comprises a first capacitor.

43. The multiple frequency oscillation circuit of claim 42 wherein said attenuating device and said other attenuating device each comprises a second capacitor.

44. The multiple frequency oscillation circuit of claim 35 wherein said frequency dependent coupling circuit generate phase shifts of the first and

second fundamental frequencies.

45. The multiple frequency oscillation circuit of claim 44 wherein the first and second fundamental frequencies 90° out of phase.

- 5 46. A quadrature oscillator circuit having low phase noise, comprising:
- 10 a first oscillator having a first fundamental frequency comprising:
- a first frequency dependent amplifier device;
- a first frequency dependent feedback device in communication with
- an output of said first frequency dependent amplifier and an
- input of said first frequency dependent amplifier to feed a
- portion of an amplified signal having the first fundamental
- frequency to an input of said first frequency dependent
- amplifier; and
- 15 a first attenuating device in communication with said first frequency
- dependent gain amplifier for reducing the gain of said first
- frequency dependent amplifier for signals having frequencies
- much less than the first fundamental frequency to decrease said
- phase noise; and
- 20 a first frequency dependent coupling circuit having a second
- fundamental frequency having an input in communication with the
- output of the first frequency dependent amplifier, said first
- frequency dependent coupling circuit comprising:

a second frequency dependent amplifier, and
a second attenuating device in communication with said second
frequency dependent amplifier for reducing the gain of said
frequency dependent amplifier for signals having frequencies
much less than said second fundamental frequency to decrease
said phase noise;

a second oscillator to generate a second fundamental signal having a
third fundamental frequency having low phase noise, in
communication with an output of the second frequency dependent
amplifier, and said second oscillator comprising:

a third frequency dependent amplifier;

a third frequency dependent feedback device in communication
with an output of said third frequency dependent amplifier and
an input of said third frequency dependent amplifier to feed a
portion of an amplified signal having the third fundamental
frequency to an input of said third frequency dependent
amplifier; and

a third attenuating device in communication with the third frequency
dependent amplifier for reducing the gain of said third frequency
dependent amplifier for signals having frequencies much less
than the third fundamental frequency to decrease the phase
noise; and

a second frequency dependent coupling circuit having a fourth

fundamental frequency having an input in communication with the output of said third frequency dependent amplifier and the input of said first frequency dependent such that a phase of the third fundamental frequency is reversed relative to a phase of the first fundamental frequency, and comprising:

a fourth frequency dependent amplifier, and

a fourth attenuating device in communication with said second frequency dependent amplifier for reducing the gain of said fourth frequency dependent amplifier for signals having frequencies much less than said fourth fundamental frequency to decrease the phase noise.

47. The quadrature oscillator circuit of claim 46 wherein said first, second, third and fourth attenuating devices each has a characteristic such that the first, second, third and fourth fundamental frequencies are each from approximately ten times to twenty times a high pass bandwidth of a respective combination of said first frequency dependent amplifier and said first attenuating device and of said third frequency dependent amplifier and said third attenuating device.

48. The quadrature oscillator circuit of claim 46 wherein said first, second, third and fourth frequency dependent amplifiers each amplify an input signal by respective predetermined gain factors, wherein said first,

second, third and fourth frequency dependent each comprises:

a frequency dependent gain determining impedance in communication with a corresponding one of said first, second, third and fourth frequency dependent amplifiers, wherein the maximum gain of each of said corresponding one of said first, second, third and fourth frequency dependent amplifiers, occurs at a respective one of the first, second, third and fourth fundamental frequencies.

49. The quadrature oscillator circuit of claim 48 wherein each of said first, second, third and fourth amplifiers comprises:

a pair of cross-coupled MOS transistors whereby a drain of each of pair of cross-coupled MOS transistors is in communication with a gate of another one of said pair of cross-coupled MOS transistors and said frequency dependent gain determining impedance;

a first current source in communication with a source of one of said pair of cross-coupled MOS transistors and to a first terminal of a voltage source and to a first terminal of a respective one of said first, second, third and fourth attenuating devices; and

a second current source in communication with a source of the other one of said pair of cross-coupled MOS transistors and to a first terminal of a respective one of said first, second, third and fourth attenuating devices.

50. The quadrature oscillator circuit of claim 48 wherein each of said first, second, third and fourth frequency dependent gain determining impedances comprises:

at least one inductor in communication with of a respective one of said

first, second, third and fourth amplifiers and a second terminal of the voltage source; and

at least one capacitor in communication with of a respective one of said first, second, third and fourth amplifiers and a third terminal of the voltage source.

51. The quadrature oscillator circuit of claim 50 wherein each of said first, second, third and fourth attenuating devices comprises a capacitor.

52. The quadrature oscillator circuit of claim 48 wherein each of said first, second, third and fourth amplifiers comprises:

an additional attenuating device;

a first cross-coupled pair of MOS transistors of the first conductivity type having a drain of each of said first cross-coupled pair of MOS transistors in communication with a gate of the other of said first cross-coupled pair of MOS transistors and to a terminal of a respective one of said first, second, third and fourth frequency dependent gain determining impedances;

a first current source in communication with a source of one of said

first cross-coupled pair of MOS transistors and to a first terminal of a respective one of said first, second, third and fourth attenuating devices;

a second current source in communication with a source of the other said first cross-coupled pair of MOS transistors and to a terminal of a respective one of said first, second, third and fourth attenuating devices;

a second cross-coupled pair of MOS transistors of the second conductivity type have a drain of each of second cross-coupled pair of MOS transistors in communication with a gate of the other of second cross-coupled pair of MOS transistors and to the first terminal of a respective one of said first, second, third and fourth attenuating devices;

a third current source in communication with a source of one of said second cross-coupled pair of MOS transistors and to a first terminal of said additional attenuating device; and

a fourth current source in communication with a source of the other of said second cross-coupled pair of MOS transistors and to a second terminal of said additional attenuating device.

53. The quadrature oscillator circuit of claim 52 wherein said first, second, third and fourth attenuating devices each comprises a first capacitor.

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57. The differential amplifier of claim 56 wherein a high pass bandwidth of

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said differential amplifier is determined as follows:

$$BW = \frac{g_m}{2\pi Cc}$$

where:

BW is the high pass bandwidth,

g_m is the transconductance of said first and second transistors as measured at the third terminals, and

Cc is the value of said capacitor,

wherein the high pass bandwidth is less than a cutoff frequency of a circuit employing said differential amplifier.

58. The differential amplifier of claim 57 wherein the cutoff frequency of the circuit containing said differential amplifier is from approximately 10 times to approximately 20 times the high pass bandwidth of said differential amplifier.

59. The differential amplifier of claim 56 wherein said first and second transistors are selected from the group of transistors consisting of NMOS transistors, PMOS transistors, and bipolar transistors.

60. The differential amplifier of claim 56 wherein said first and second biasing sources are selected from the group of biasing sources consisting of

current sources and resistors.

61. An oscillator having a fundamental frequency and having low phase noise comprising:

frequency dependent amplifier means for amplifying a signal;
frequency dependent feedback means for providing feedback to said
frequency dependent amplifying means; and
attenuating means for attenuating noise signals having a frequency
much less than the fundamental frequency.

62. A method of generating a signal having a fundamental frequency with low noise comprising the steps of:

(a) amplifying a signal;
(b) providing feedback to step (a); and
(c) attenuating the signal in step (a) having a frequency much less
than the fundamental frequency.

63. An LC oscillator having a fundamental frequency and having low phase noise comprising:

a frequency dependent amplifier comprising:

a pair of cross-coupled MOS transistors of a first conductivity
type, a drain of each of said pair of cross-coupled MOS
being in communication with a gate of the other one of said
pair of cross-coupled MOS transistors,

a first current source in communication with a source of one of
said pair of cross-coupled MOS transistors, wherein said first
current source comprises a first programmable resistance,
and

a second current source in communication with a source of
another of said pair of cross-coupled MOS transistors,
wherein said second current source comprises a second
programmable resistance;

a frequency dependent gain determining circuit comprising

a first inductor in communication with the drain of said one of
said pair of cross-coupled MOS transistors and a first
terminal of a voltage source,

a second inductor in communication with the drain of the other
of said pair of cross-coupled MOS transistors and the first
terminal of the voltage source,

a first capacitor in communication with the drain of said one of
said of said pair of cross-coupled MOS transistors and a
second terminal of the voltage source, and

a second capacitor in communication with the drain of the other
of said pair of cross-coupled MOS transistors and the
second terminal of the voltage source.

64. The LC oscillator of the claim 63, wherein the first current source comprises
a first inductor in communication with said first programmable resistance, and

wherein the second current source comprises a second inductor in communication with said second programmable resistance.

65. An LC oscillator having a fundamental frequency and having low phase noise comprising:

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a frequency dependent amplifier comprising:

a pair of cross-coupled MOS transistors of a first conductivity type, a drain of each of said pair of cross-coupled MOS being in communication with a gate of the other one of said pair of cross-coupled MOS transistors,

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a first current source in communication with a source of one of said pair of cross-coupled MOS transistors, wherein said first current source comprises a first programmable inductance, and

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a second current source in communication with a source of another of said pair of cross-coupled MOS transistors, wherein said second current source comprises a second programmable inductance;

a frequency dependent gain determining circuit comprising

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a first inductor in communication with the drain of said one of said pair of cross-coupled MOS transistors and a first terminal of a voltage source,

a second inductor in communication with the drain of the other of said pair of cross-coupled MOS transistors and the first

*Sub
A8*

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terminal of the voltage source,
a first capacitor in communication with the drain of said one of
said of said pair of cross-coupled MOS transistors and a
second terminal of the voltage source, and
a second capacitor in communication with the drain of the other
of said pair of cross-coupled MOS transistors and the
second terminal of the voltage source.

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66. The LC oscillator of the claim 10, wherein said first current source comprises
a first programmable resistance, and wherein said second current source
comprises a second programmable resistance.

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67. The LC oscillator of the claim 10, wherein said first current source comprises
a first programmable inductance, and wherein said second current source
comprises a second programmable inductance.